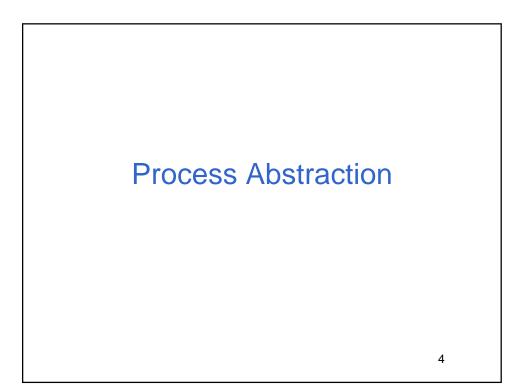


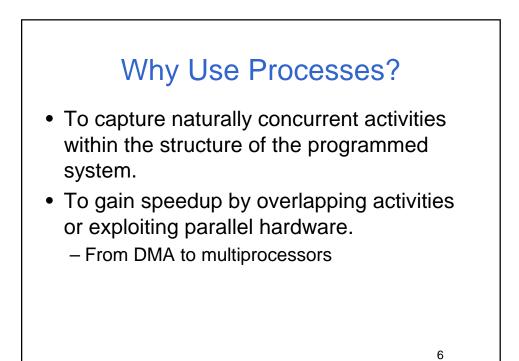


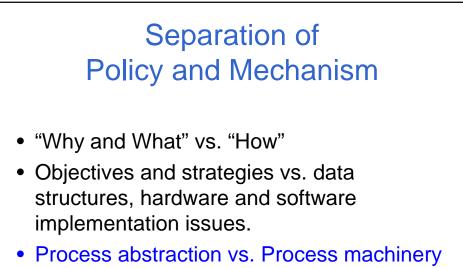
- Processes thread of control with context
- Files everything else
 - Regular file named, linear stream of data bytes
 - Sockets endpoints of communication, possible between unrelated processes
 - Pipes unidirectional I/O stream, can be unnamed
 - Devices

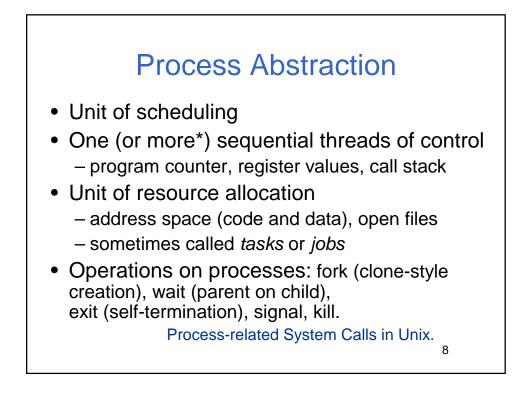


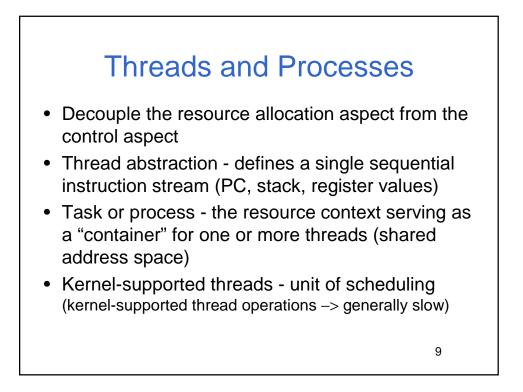


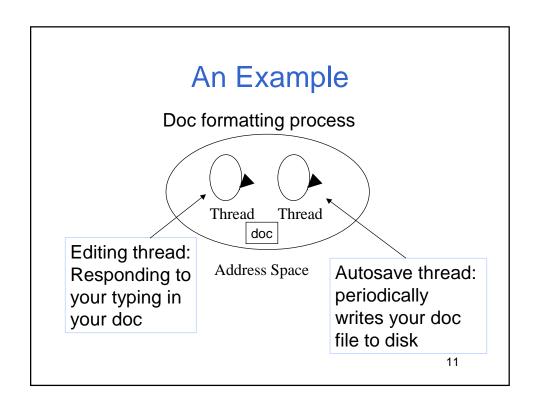
- Processes are the OS-provided abstraction of multiple tasks (including user programs) executing concurrently.
- One instance of a program (which is only a passive set of bits) *executing* (implying an execution context – register state, memory resources, etc.)
- OS schedules processes to share CPU.

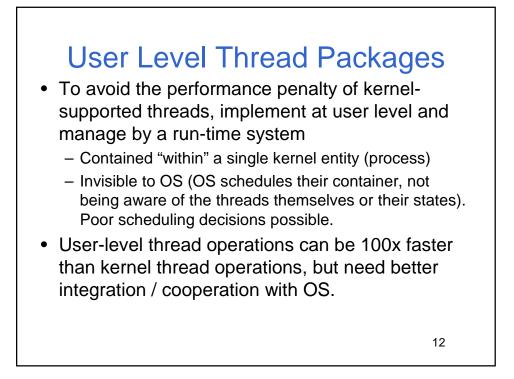


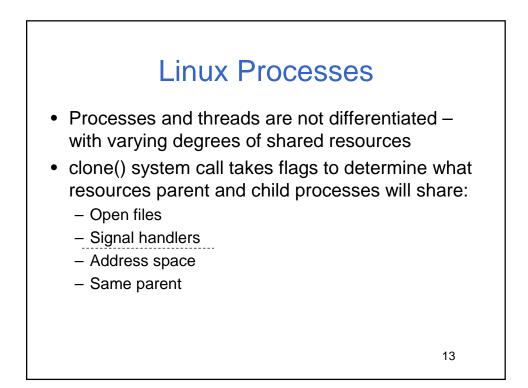


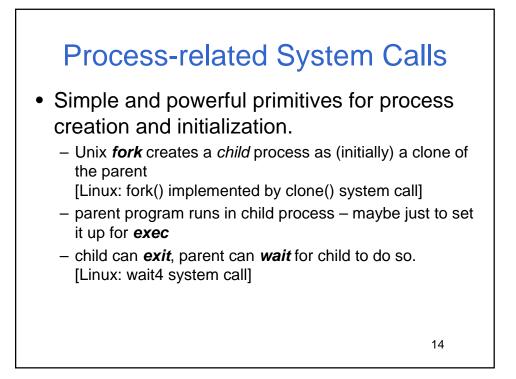


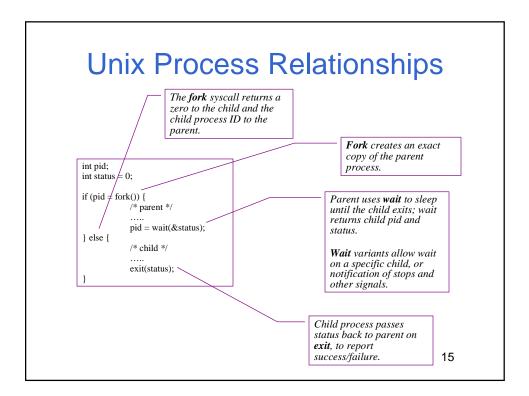


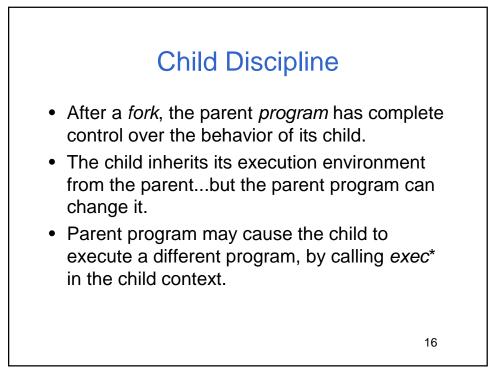


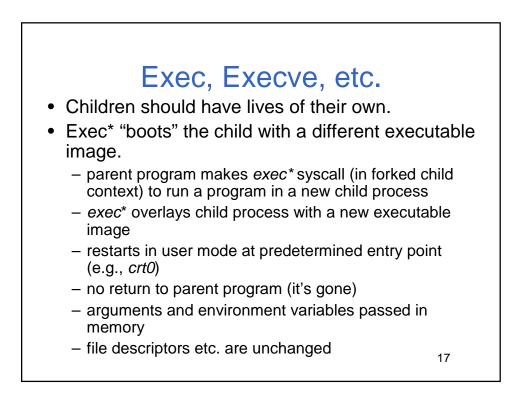


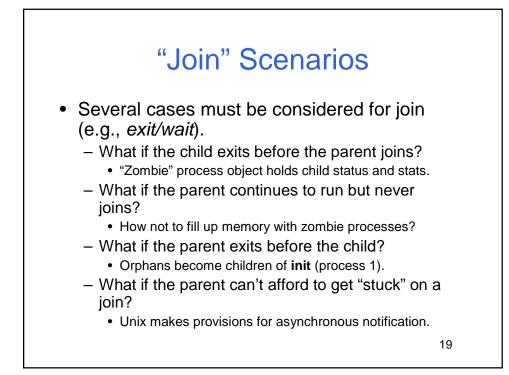


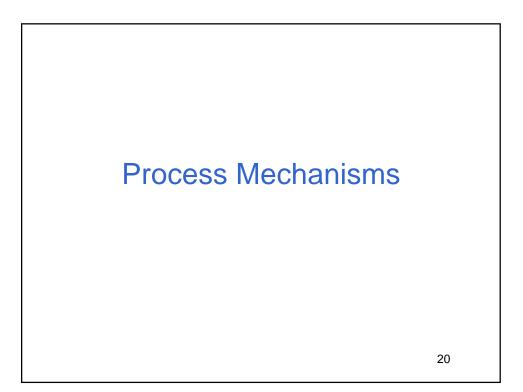










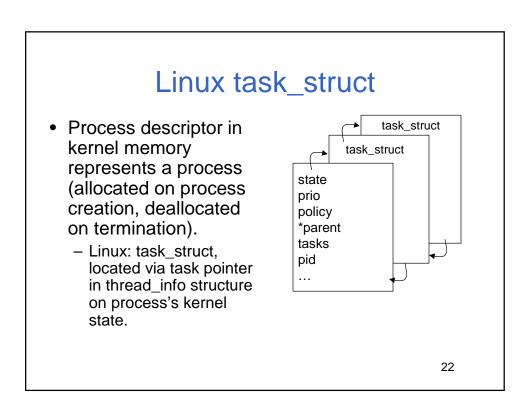


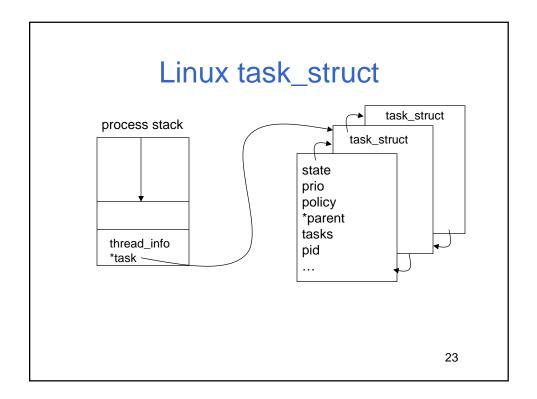
Context Switching

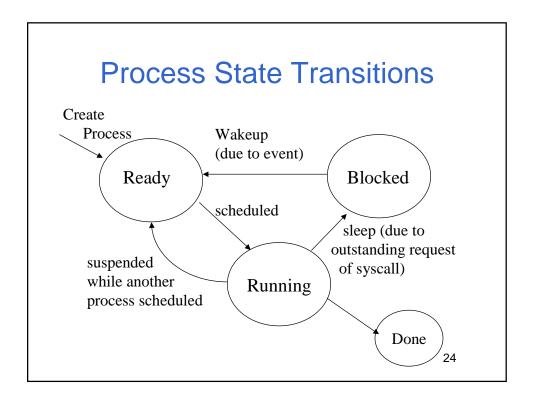
- When a process is running, its program counter, register values, stack pointer, etc. are contained in the hardware registers of the CPU. The process has direct control of the CPU hardware for now.
- When a process is not the one currently running, its current register values are saved in a process descriptor data structure (task_struct)

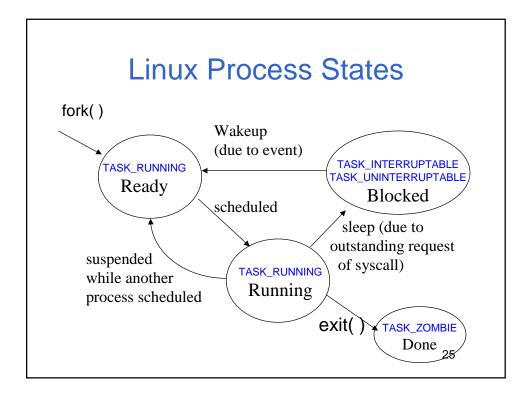
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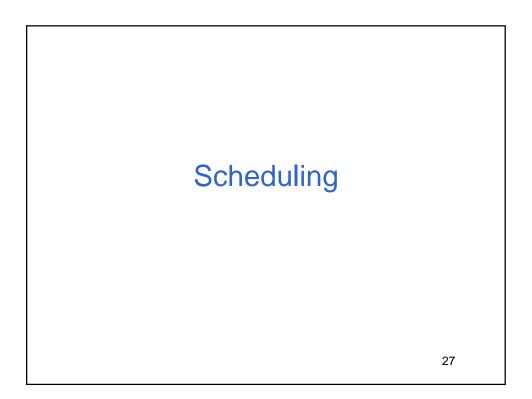
 Context switching involves moving state between CPU and various processes' descriptors by the OS.

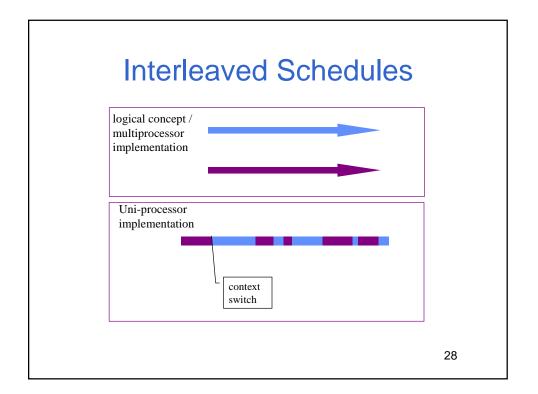


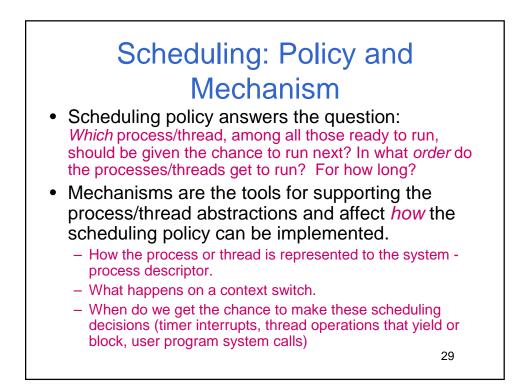






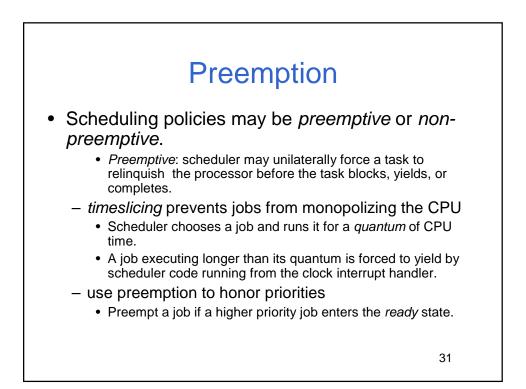


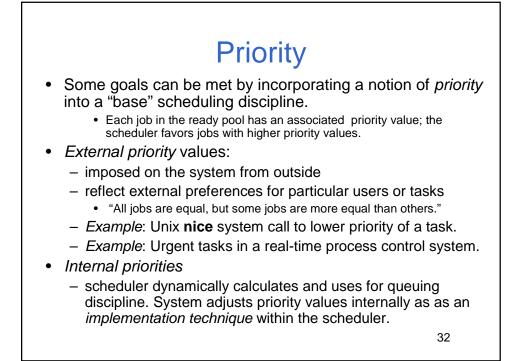


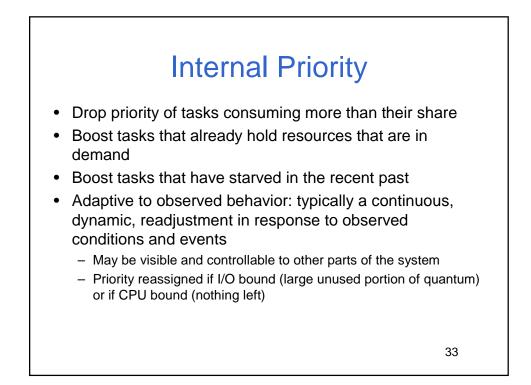


Flavors

- Long-term scheduling which jobs get resources (e.g. get allocated memory) and the chance to compete for cycles (to be on the ready queue).
- Short-term scheduling or process scheduling - which of those gets the next slice of CPU time
- **Non-preemptive** the running process/thread has to explicitly give up control
- Preemptive interrupts cause scheduling opportunities to reevaluate who should be running now (is there a more "valuable" ready task?)

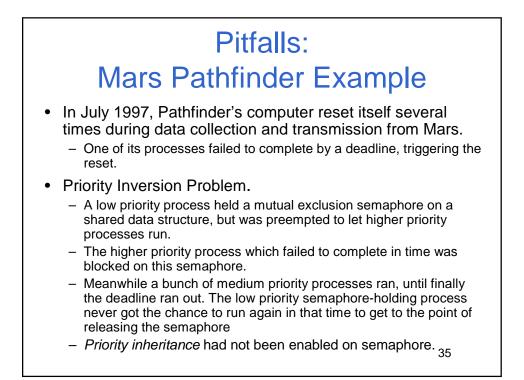


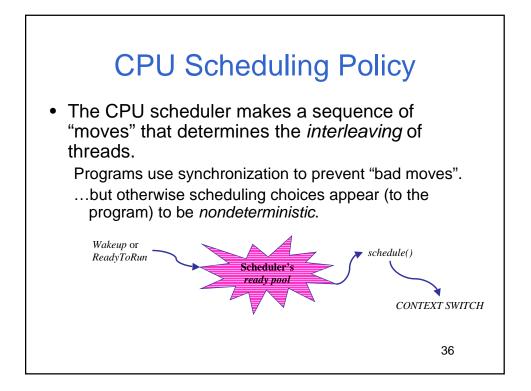


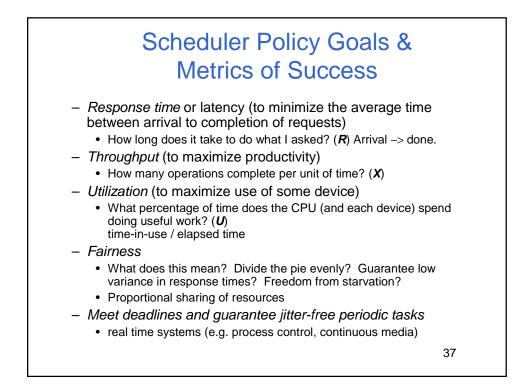


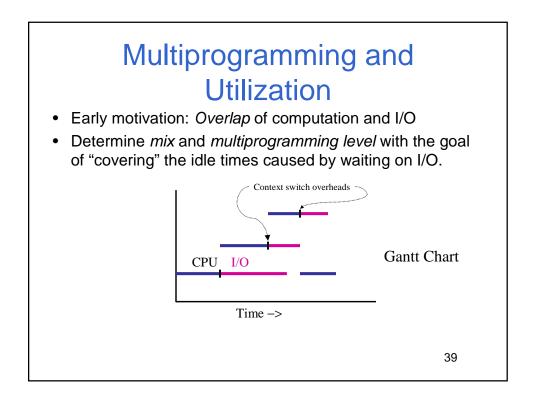


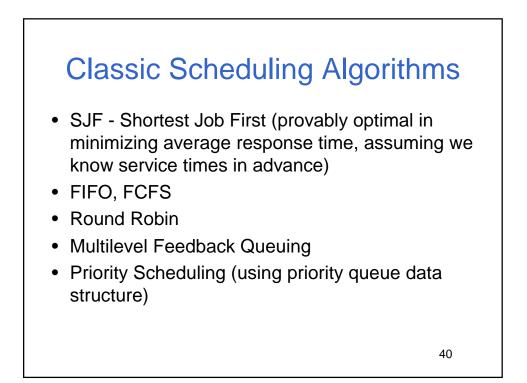
- Priorities must be handled carefully when there are dependencies among tasks with different priorities.
 - A task with priority *P* should never impede the progress of a task with priority *Q* > *P*.
 - This is called *priority inversion*, and it is to be avoided.
 - The basic solution is some form of *priority inheritance*.
 - When a task with priority *Q* waits on some resource, the holder (with priority *P*) temporarily inherits priority *Q* if *Q* > *P*.
 - Inheritance may also be needed when tasks coordinate with IPC.
 - Inheritance is useful to meet deadlines and preserve low-jitter execution, as well as to honor priorities.

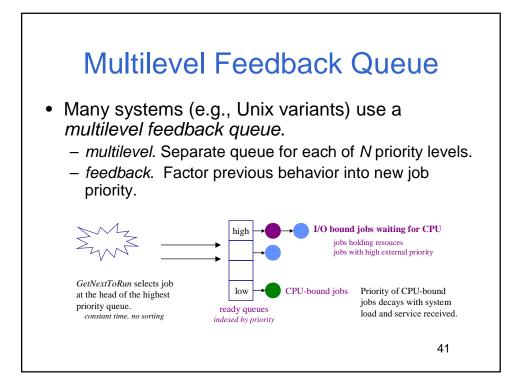


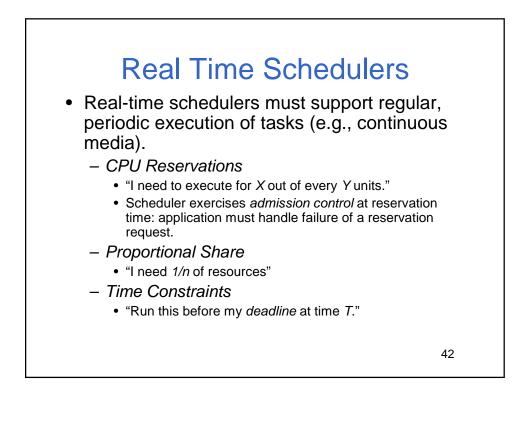


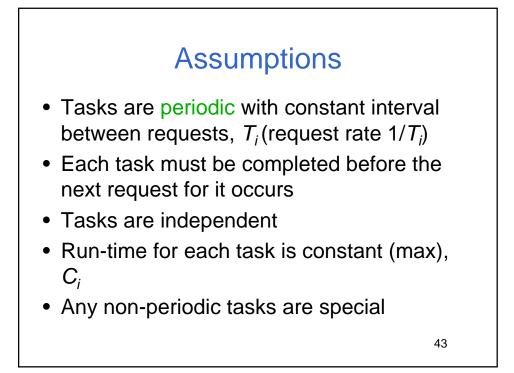


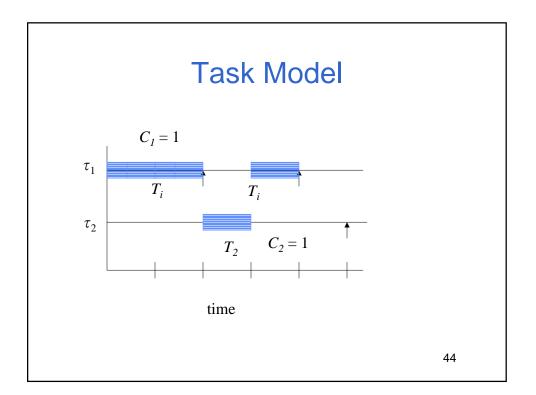


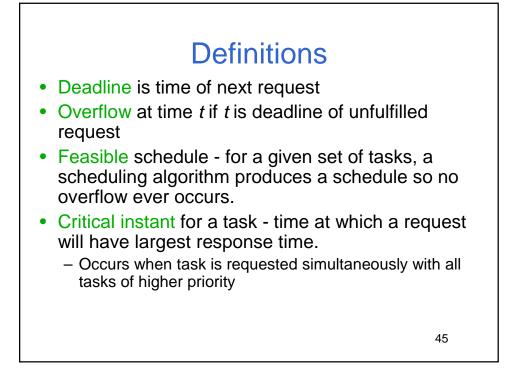


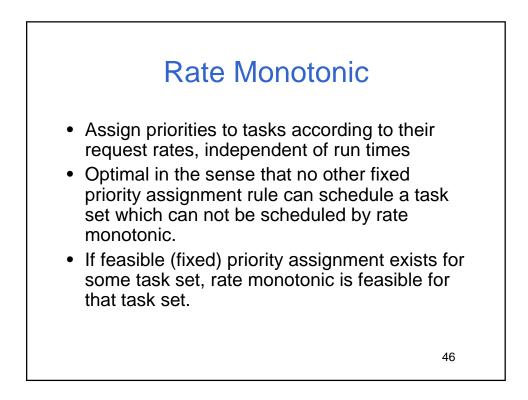


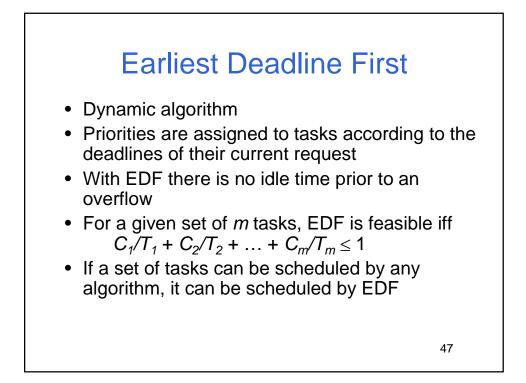


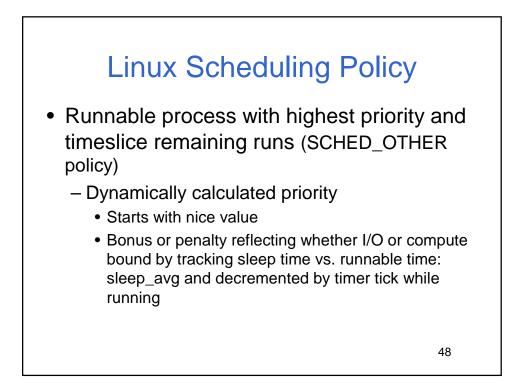


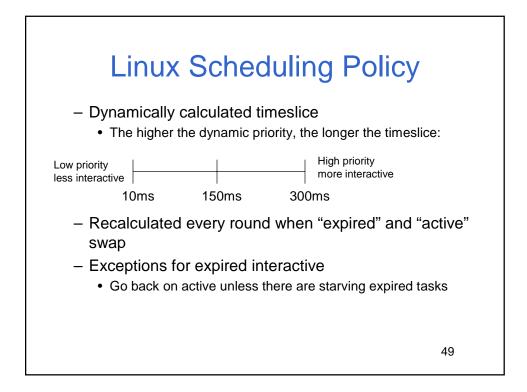


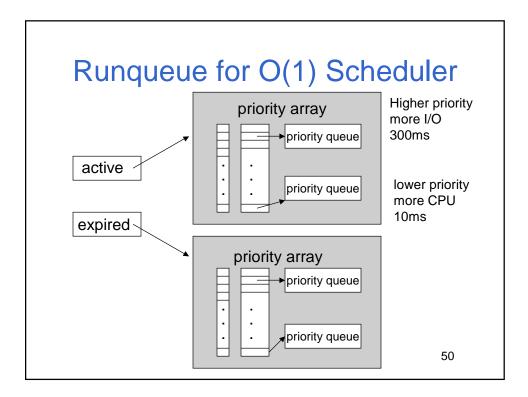


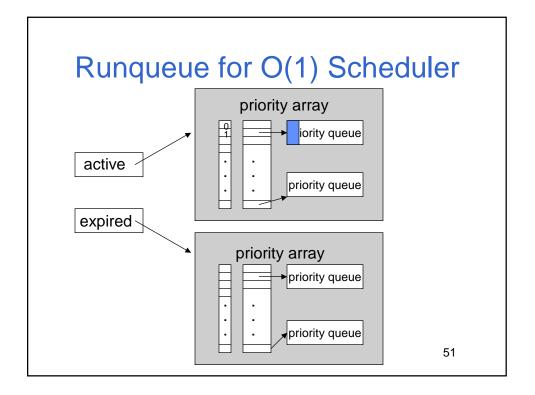


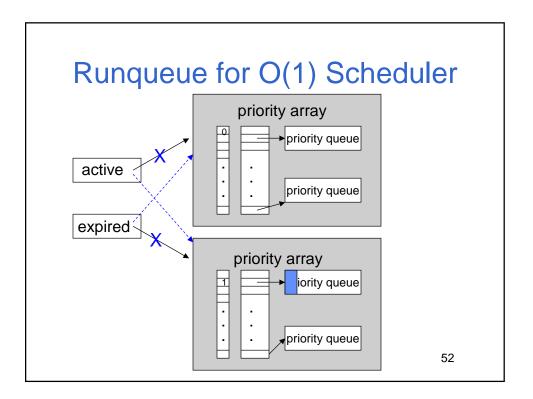


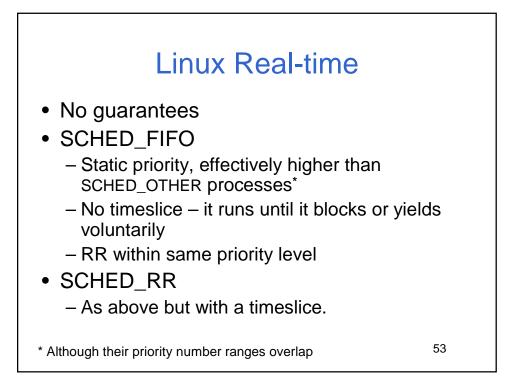


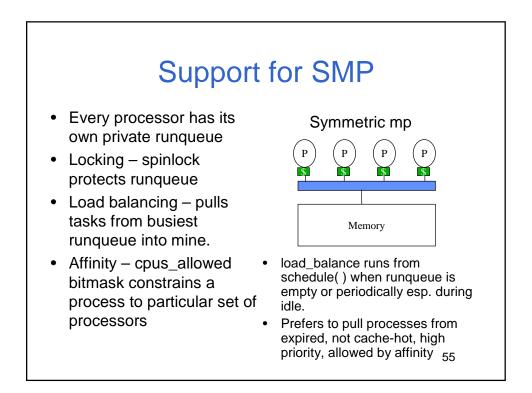


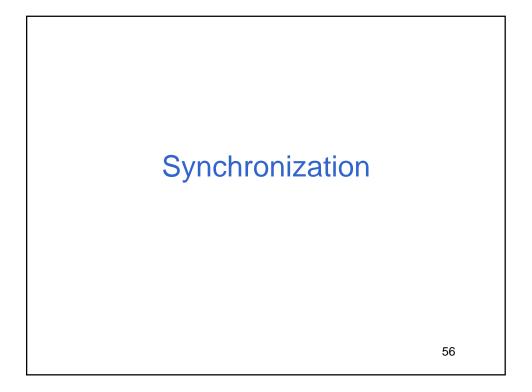


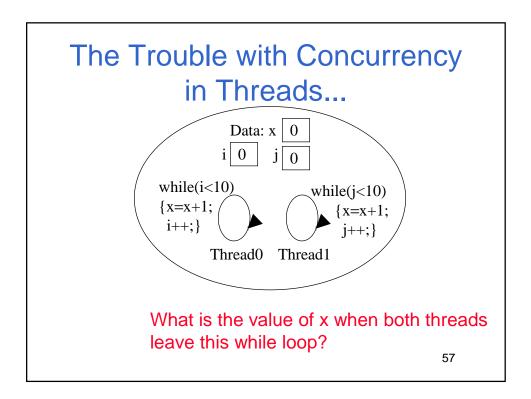




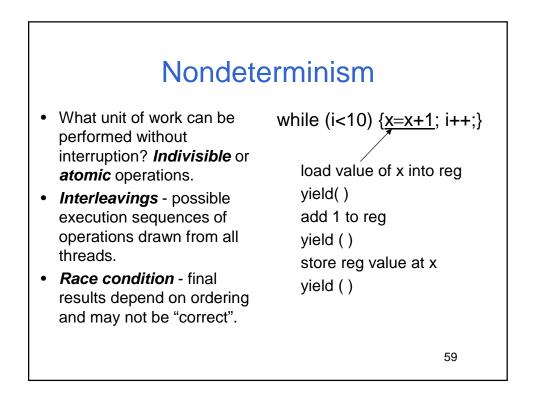








| Range of Answers | | |
|---|--------------------------------|------------------------|
| Process 0 | Process1 | |
| LD x // x currently 0 | | |
| | LD x Add 1 | // x currently 0 |
| | ST x | // x now 1 |
| | Do 8 more full loops $//x = 9$ | |
| Add 1 | | |
| ST x // x now 1, stored over 9 | | |
| | LD x | // x now 1 |
| Do 9 more full loops // leaving x at 10 | | |
| | Add 1 | |
| | ST x | //x = 2 stored over 10 |
| | | 58 |





- On a uniprocessor, the possible execution sequences depend on when context switches can occur
 - Voluntary context switch the process or thread explicitly yields the CPU (blocking on a system call it makes, invoking a Yield operation).
 - Interrupts or exceptions occurring an asynchronous handler activated that disrupts the execution flow.
 - Preemptive scheduling a timer interrupt may cause an involuntary context switch at any point in the code.
- On multiprocessors, the ordering of operations on shared memory locations is the important factor.

